

PTO/SB/30 (5/2000)

Approved for use through 09/30/2000. OMB 0651-0031  
U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Under the paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

# REQUEST FOR CONTINUED EXAMINATION (RCE) TRANSMITTAL

Subsection (b) of 35 U.S.C. § 132, effective on May 29, 2000,  
provides for continued examination of an utility or plant application  
filed on or after June 8, 1998.  
See The American Inventors Protection Act of 1999 (AIPA).

Application Number	09/452,691
Filing Date	12/02/1999
First Named Inventor	BAILEY
Group Art Unit	2815
Examiner Name	Fenty
Attorney Docket Number	T1-27935

This is a Request for Continued Examination (RCE) under 37 C.F.R. § 1.114 of the above-identified application.

**NOTE:** 37 C.F.R. § 1.114 is effective on May 29, 2000. If the above-identified application was filed prior to May 29, 2000, applicant may wish to consider filing a continued prosecution application (CPA) under 37 C.F.R. § 1.53 (d) (PTO/SB/29) instead of a RCE to be eligible for the patent term adjustment provisions of the AIPA. See Changes to Application Examination and provisional Application Practice, Interim Rule, 65 Fed. Reg. 14865, (Mar. 20, 2000), 1233 Off. Gaz. Pat. Office 47 (Apr. 11, 2000), which established RCE practice.

## 1 Submission required under 37 C.F.R. § 1.114

a ☐ Previously submitted

- i. ☐ Consider the amendments(s)/reply under 37 C.F.R. § 1.116 previously filed on \_\_\_\_\_  
(Any unentered amendment(s) referred to above will be entered).
- ii. ☐ Consider the arguments in the Appeal Brief or Reply Brief previously filed on \_\_\_\_\_
- iii. ☐ Other \_\_\_\_\_

b ☒ Enclosed

- i. ☒ Amendment/Reply
- ii. ☐ Affidavit(s)/Declaration(s)
- iii. ☐ Information Disclosure Statement (IDS)
- iv. ☐ Other \_\_\_\_\_

## 2 Miscellaneous

a ☐ Suspension of action on the above-identified application is requested under 37 C.F.R. § 1.103 (c) for a period of \_\_\_\_\_ months. (Period of suspension shall not exceed 3 months; Fee under 37 C.F.R. § 1.17 (i) required)b ☐ Other \_\_\_\_\_

## 3 Fees


The RCE fee under 37 C.F.R. § 1.17(e) is required by 37 C.F.R. § 1.114 when the RCE is filed.

a ☒ The Director is hereby authorized to charge the following fees, or credit any overpayments, to Deposit Account No. 20-0888, Texas Instruments Incorporated.

- i. ☒ RCE fee required under 37 C.F.R. § 1.17(e)
- ii. ☒ Extension of time fee (37 C.F.R. §§ 1.136 and 1.17)
- iii. ☐ Other \_\_\_\_\_

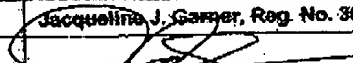
b ☐ Check in the amount of \$ \_\_\_\_\_ enclosedc ☐ Payment by credit card (Form PTO-2038 enclosed)

## SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT REQUIRED

Name (Print / Type)	Jacqueline J. Garner	Registration No. (Attorney / Agent)	36,144
Signature		Date	11/3/2003

## CERTIFICATE OF MAILING OR TRANSMISSION

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Box RCE, PO Box 1450, Alexandria, VA 22313-1450, or facsimile transmitted to the U.S. Patent and Trademark Office on:

Name (Print or Type)	Jacqueline J. Garner, Reg. No. 36,144
Signature	
Date	11/3/2003

**Burden Hour Statement:** This form is designed to take 0.2 hours to complete. Firms will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND Fees and Completed Forms to the following address: Commissioner for Patents, Box RCE, Washington, DC 20231.

To: Technology Center 2800  
Facsimile Number: 703-872-9319

Total Pages Sent 13

From: Jacqueline J. Garner  
Texas Instruments Incorporated  
Facsimile: 972-917-4418  
Phone: 972-271-1176

RECEIVED  
CENTRAL FAX CENTER

NOV 03 2003

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.: 09/452,691  
Applicant: Fred D. Bailey et al.  
Filed: 12/02/1999  
TCA/U.: 2815  
Examiner: Jesse A. Fenty


Confirmation No. 1638

OFFICIAL

Docket No.: TI-27935  
Customer No.: 23494  
Title: AN INTEGRATED CIRCUIT HAVING A THIN FILM RESISTOR LOCATED WITHIN  
A MULTILEVEL DIELECTRIC BETWEEN AN UPPER AND LOWER METAL INTERCONNECT LAYER

## CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by  
facsimile to the U.S. Patent and Trademark Office at 703-872-  
9319 on the date shown below:

  
Jacqueline J. Garner, Reg. No. 36,144

November 3, 2003  
Date

## FACSIMILE COVER SHEET

<input checked="" type="checkbox"/> FACSIMILE COVER SHEET	<input checked="" type="checkbox"/> AMENDMENT (8 Pages)
<input type="checkbox"/> NEW APPLICATION	<input checked="" type="checkbox"/> EOT 1st (3 Pages)
<input type="checkbox"/> DECLARATION (# Pages)	<input type="checkbox"/> NOTICE OF APPEAL (# Pages)
<input type="checkbox"/> ASSIGNMENT (# Pages)	<input type="checkbox"/> APPEAL (# Pages)
<input type="checkbox"/> FORMAL DRAWINGS	<input type="checkbox"/> ISSUE FEE (# Pages)
<input type="checkbox"/> INFORMAL DRAWINGS	<input type="checkbox"/> REPLY BRIEF (IN TRIPLICATE) (# Pages)
<input type="checkbox"/> CONTINUATION APP'N (# Pages)	<input checked="" type="checkbox"/> RCE
<input type="checkbox"/> DIVISIONAL APP'N	
NAME OF INVENTOR(S): Bailey	
TITLE OF INVENTION: An Integrated Circuit Having ...	
TI FILE NO.: TI-27935	DEPOSIT ACCT. NO.: 20-0668
RECEIPT DATE & SERIAL NO.: Serial No.: 09/452,691 Filing Date: 12/02/1999	
FAXED: 11/3/2003 DUE: 10/2/2003 ATTY/SECY: JRG	

This facsimile is intended only for the use of the address named and contains legally privileged and/or confidential information. If you are not the intended recipient of this telecopy, you are hereby notified that any dissemination, distribution, copying or use of this communication is strictly prohibited. Applicable privileges are not waived by virtue of the document having been transmitted by Facsimile. Any misdirected facsimiles should be returned to the sender by mail at the address indicated on this cover sheet.

Texas Instruments Incorporated  
PO Box 655474, M/S 3999  
Dallas, TX 75074